



Mrs. Sony Venugopal

Assistant Professor

Experience (Teaching and Industry)

- RNSIT (2019-till date)
- DBIT, Bangalore (2007-2019)
- SNGCET, KERALA (2006-2007)
- GOVT. POLYTECHNIC COLLEGE, KERALA (2005-2006)

Qualifications

- B.Tech in Electrical and Electronics Engineering from NSS College of Engineering, Kerala.
- M.Tech in Control Systems from St. Mary's College of Engineering, Hyderabad.
- PhD –Pursing in Department of Electrical and Electronics Engineering affiliated to Jain University.

Specialization (Academics)

- Analog Electronics Circuits
- Digital Signal Processing
- Control Systems
- Microcontroller
- Basic Electrical Engineering

Specialization (Research)

- Cyber Security
- Power Systems

Publications

1. Guarding the Grid: Exploring Advanced Strategies for Detecting Intrusions in Power Distribution Devices ,Sony Venugopal,Dr. Hannah Jessie Rani. R, Roopa. K.R,INTERNATIONAL JOURNAL OF INNOVATIVE RESEARCH 13 (Issue 8,), 14998-15004 in 2024
2. Low power technique for testing VLSI circuits K R Roopa, Dr.Hannah Jessie Rani, Sony Venugopal ,Taylor Series, Computer Science Engineering, 99-107
3. Maximum power point tracking for photovoltaic systems Sony Venugopal, Akshay S Aspilli, R Raveendra Proceeding of the International Conference on Current Trends in Eng in 2017
4. Published patent on IoT-Based Smart Gas Leakage Detection and Valve Control System (Patent Application no : 202541069110) in July 2025

Awards Received

- Best paper award for the paper titled “Guarding the Grid: Exploring Advanced Strategies for Detecting Intrusions in Power Distribution Devices for the Internal conference held at RRCE, Bangalore.