



Dhanusha A

Assistant Professor

Experience (Teaching and Industry)

RNSIT (2019- till date)
INDIAN NAVAL ACADEMY (2018)
CUIET(2015-2016)
MIT MANIPAL(2015)
KIET GROUP OF INSTITUTIONS(2014-2015)

Qualifications

BTech from Vimal Jyothi engineering College, Kerala
M.Tech from Vedavyasa Institute of technology, Calicut, Kerala
Pursuing PhD from VTU ,RNSIT as Research Centre

Specialization (Academics)

VLSI Design
Analog Communication
Digital System Design

Specialization (Research)

VLSI

Publications

1.Dhanusha A, Vipula Singh , "Advancements in VLSI implementation of ECG signal compression schemes" [2024 IEEE International Conference on Signal Processing, Informatics, Communication and Energy Systems \(SPICES\)](#)

DOI: [10.1109/SPICES62143.2024.10779673](https://doi.org/10.1109/SPICES62143.2024.10779673)

2. Dhanusha .A "An Application Of Power Reduction Using multibit flipflop on PRBS generator Circuit" International journal of Scientific Research ISSN No.2277-8179 Volume 3 Issue 5 p60-63, May 2014

https://www.researchgate.net/publication/274929778_An_Application_of_Power_Reduction_Using_Multi_Bit_Flip_Flop_on_Prbs_Generator_Circuit

Awards Received

Awarded Elite in the NPTEL Certification Course on Evolution of Air Interface Towards 5G -2024

Subrahmanya Award for Best Student -2006