



**RNS INSTITUTE OF TECHNOLOGY**  
(AICTE Approved, VTU Affiliated and NAAC 'A' Accredited)  
**Department of Electronics & Communication Engineering.**  
*Program is Accredited by NBA for the Academic years 2018-19, 2019-20 and 2020-2021*

## *Center of excellence in VLSI*

### **Vision:**

To achieve excellence in the area of VLSI design for circuits and systems.

### **Mission:**

- To induce creative thinking amongst all stake holders.
- To provide frame work for collaborative and multidisciplinary research.

### **Objectives:**

- To provide state-of-the-art resources that contribute to achieve excellence in teaching-learning, research and development activities
- To bridge the gap between industry and academia through MOU and tie-ups.
- To promote entrepreneurship through innovation to address social issues.
- To provide suitable forums to enhance the creative talents of students and faculty members.
- To inculcate ethics and moral values among the faculty and students.

### **Weblink of Center Excellence in VLSI:**

<https://coevlsi-rnsitece.blogspot.com/>

### **Activities carried out under Center of Excellence in VLSI**

- Inauguration of Center of Excellence

Department of Electronics and Communication Engineering, RNS Institute of Technology, Bengaluru initiated the center of Excellence in VLSI. The COE in VLSI is inaugurated by Mr. Pramod Notiyath, Manager, Corporate Applications, Synopsys, on 4th May 2018.

- Expert lectures in the area of VLSI
- Research based projects
- Internships

2 students took internship in Intel.

11 students took internship in VLSI by kanada technologies.

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- Advanced courses in VLSI by industry experts and faculty members.

Center initiated the following technical training course with the help of experts from the industries (kanada technologies) for the 5<sup>th</sup> semester students. The course with hands on experience was conducted.

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- Promoting faculties to submit research proposals for different funding agencies
  - Students projects.
  - Skill development training
    - Students of 5<sup>th</sup> semester and two faculty members visited NAL as part of Industry visit.
    - One day workshop on Latex was conducted on 12-04-2019 for the benefit faculty members of RNSIT. Dr. Rajini and Mr. Sanjay were the resource persons.
    - Students of UG/PG and Faculty members have presented papers in the National conference organized by ECE dept, RNSIT, held on 03/05/2019.
    - Three days workshop on Analog and Digital VLSI Design for staff.
    - One day workshop on VLSI Design and Simulation for digital Applications for UG students

- Two days workshop on Digital Design and Layouts for students.
  - Two days workshop on Digital VLSI Design for PG students.
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- Interdisciplinary activities

- Research publication.

- Workshops.

- Three days workshop on Analog and Digital VLSI Design for staff.
  - One day workshop on VLSI Design and Simulation for digital Applications for UG students
  - Two days workshop on Digital Design and Layouts for students.
  - Two days workshop on Digital VLSI Design for PG students.
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