

RNS Institute of Technology, Channasandra, Bangalore-560098
ECE Dept | RESUME - Faculty

Name	MANJUNATHA NAIK V		
Date of Birth	28-05-1991		
Address	Contact Address: Assistant Professor, Department of ECE, RNSIT, Bangalore-98 E-mail: manjunathnaikv@rnsit.ac.in , manjunathanaikv@gmail.com		
Subject / Discipline/ Major	Electronics and Communication Engineering		
Educational Qualifications	Exam Passed	Institution University	Year
	Degree: BE (Electronics and communication Engineering)	APS College of Engineering VTU	2009 - 2013
	PG: M.Tech (VLSI Design and Embedded System)	JSS Academy of Technical Education VTU	2013 - 2015
	Higher: Ph.D. (pursuing)	Research Centre : Dept. of ECE, Dayananda Sagar College of Engineering, Bangalore VTU	(2019 registered)
Experience	Nature of Experience	No. of Years	
	Teaching	5	
	Industry/Research	-	
	Total No. of years of Experience	5	
Experience Details	Designation	Institution/ Organization	Duration
	Assistant professor	RNSIT	1 August 2016 to till now
Professional bodies Membership details			
Other Professional Experiences	Delivered a talk on “ASIC Design” in the workshop “Analog and Digital IC: Design flow using Mentor graphic EDA tool” organized by Dept. of ECE, DSCE held on 10.03.2018.		
Areas of Research Interest & Guidance	VLSI Design and Embedded System		
National/ International Work Shops/ Seminars / Conferences Attended	Webinars : <ul style="list-style-type: none"> • Participated in online webinar on “Chip Design and its Applications” held on 28-05-2020 organized by dept. of ECE, SJBIT, Bangalore • Participated in online webinar on “Power for CMOS VLSI Circuits” organized by IEEE Circuits and systems society, Bangalore section held on 20th June 2020. • Participated in webinar series on “Satellite and automation” organized by GSSSIETW IEEE Student chapter, Mysore held during 11th to 13th MAY 2020. Faculty development program: <ul style="list-style-type: none"> • Participated in Two Day FDP on “Special Topics in optics and Photonics” held during January 27 – 28, 2017 organized by Dept. of ECE, RNSIT, Bengaluru • Participated in VTU sponsored FDP on “Design of Embedded systems with ARM Cortex M3” from 16th to 20th January, 2018 organized by Dept. of Telecommunication Engineering, in association with ALS and IEEE, held at Bangalore Institute of Technology. • Participated in Three Day FDP on “Python Programming and Applications” held during June 18 – 20, 2018 jointly organized by Dept. of ECE and MCA, RNSIT, Bengaluru • Participated & completed AICTE Training And Learning (ATAL) Academy Online Elementary FDP on “Formal Verification of Digital Designs” from 7th to 11th June 2021 organized by PES UNIVERSITY Workshops <ul style="list-style-type: none"> • Participated in Three Day workshop on “COMPUTER NETWORKS LABORATORY using NS3, NCTUNS and C/C++” held during Jan 23 – 25, 2018. • Attended Workshop on Internet of things held on 24.01.2019 at VTU Regional office, Bengaluru 		

**Papers Presented
/Books Published**

- Attended online Workshop on “**IEEE REFERENCE MANAGEMENT TOOL FOR RESEARCH ARTICLES AND THESIS WRITING**” organized by IEEE Bangalore section held on 20th MAY, 2020
- Published a Text book named “**VLSI design**” (co – authored) in 2019 with **ISBN 978 – 81 – 943083 – 3 – 1.**
- Presented a paper on “ **Performance analysis of Parallel Prefix adder**” in International conference(IRAJ).

**Additional Information
(if any)**

- Professional training in “**Layout Design**” by KANADA Technologies (now Epitome Circuits) and **Designed ASIC Standard Cells Library, SRAM and Analog Layouts using 45nm technology in Cadence Virtuoso.**
- Professional training by **BSNL**, held on 1/10/2019 at Satcom building, BSNL, 5th block, Jayanagar, Bangalore.
- Completed the training Program on “**Manual Testing**” and “ **Selenium**” by Softgen InfoTech Pvt. Ltd.