

Name	APARNA VISHWANATH		
Date of Birth	06/02/1991		
Address	<b>Contact Address:</b> Assistant Professor, Department of ECE, RNSIT, Bangalore-98 <b>E-mail:</b> aparnavishwanath91@gmail.com		
Subject / Discipline/ Major	Electronics and Communication Engineering		
Educational Qualifications	<b>Exam Passed</b>	<b>Institution   University</b>	<b>Year</b>
	<b>Degree: BE(Branch)</b>	<b>Mumbai university</b>	2012
	<b>PG: M.Tech(Branch)</b>	<b>Mumbai university</b>	2014
	<b>Higher: Ph.D</b>		
Experience	<b>Nature of Experience</b>	<b>No. of Years</b>	
	Teaching	7	
	Industry/Research	0	
	Total No. of years of Experience	7	
Experience Details	<b>Designation</b>	<b>Institution/ Organization</b>	<b>Duration</b>
	Assistant professor	DJ Sanghvi College of Engineering, Mumbai	Aug 2013-June 2014
	Assistant professor	RNS institute of technology	RNS institute of technology
Professional bodies Membership details	IETE		
Other Professional Experiences			
Areas of Research Interest & Guidance	<ul style="list-style-type: none"> <li>Real time Embedded Systems</li> </ul>		
National/ International Work Shops/ Seminars / Conferences Attended	<ul style="list-style-type: none"> <li>3 days FDP on "Python programming and applications" from 18 – 20, June 2018 at RNSIT, Bengaluru.</li> <li>FDP on "Special Topics in Optics and Photonics" from 27th to 28th January - 2017 (2 Days) at RNSIT, Bengaluru.</li> <li>Two-day RBK Memorial Faculty Development Programme on "<b>Antenna Design &amp; GPR Applications Using PAM-CEM</b>" at RNSIT, Bengaluru. <b>June 21 – 22, 2016</b></li> <li>Five-day ISTE Sponsored Faculty Development Programme on "<b>NS-3 and Its Advanced Usage</b>" at RNSIT, Bengaluru. <b>June 27 – July 1, 2016</b></li> <li>Four-day "<b>Symposium on Linear Algebra and its Applications in Computer Vision</b>" at JSSATE, Bengaluru. <b>July 13 – 16, 2016</b></li> <li>Two day workshop on '<b>Techniques for effective technical paper writing</b>' organized by Department of Electronics Engineering, Fr. CRCE., Bandra.</li> <li>Two day workshop on '<b>System Modeling and Simulation</b>' organized by Department of Electronics Engineering, Fr. CRCE., Bandra.</li> <li>Faculty Development Workshop on '<b>ARM mbed Platform</b>' held at Department of Electronics Engineering, Fr. CRCE., Bandra.</li> </ul>		

**Papers Presented /Books  
Published**

- Control and Synchronization of Dual Inverted Pendulum. (Reviewed + Partial implementation)
- 8 Channel Industrial Alarm Annunciator. (Implemented)
- Modeling NMOSFET using SPICE. (Implemented )
- Scheduling algorithm for real time operating systems using ACO. (Reviewed)
- Handwritten digit recognition using back propagation neural network. (Reviewed)
- Published a paper on 'Task scheduling in Homogeneous Multiprocessor Systems using Evolutionary Techniques' in International Journal of Emerging Technology and Advanced Engineering (IJETAE) in Vol. 4, Issue 2, February 2014.
- Published a paper on 'Task Scheduling in Multi-Network Multiprocessor Systems using Genetic Algorithms' in International Journal of Engineering Research and Technology (IJERT) in Vol. 3, Issue 10, October 2014.
- Presented and published paper on 'Scheduling algorithm for real time operating systems using ACO' in ICIECS '13.
- Presented and published paper on 'Handwritten digit recognition using back propagation neural network and K- Nearest Neighbor neural network' in ICIECS '13.
- Presented and published paper on '8- Channel Industrial Alarm Annunciator' in ICACACT '13

**Additional Information  
(if any)**