



RNS INSTITUTE OF TECHNOLOGY
(*AICTE Approved, VTU Affiliated and NAAC 'A' Accredited*)
Department of Electronics & Communication Engineering.
Program is Accredited by NBA for the Academic years 2018-19, 2019-20 and 2020-2021

Center of excellence in VLSI

Activities Conducted:

Department of Electronics and Communication, RNS Institute of Technology, Bengaluru, has initiated the Center of Excellence in VLSI, a platform for the benefit of students in doing research, project, technical course and workshops and Industry institute interaction etc.

Weblink of Center Excellence in VLSI:

<https://coevlsi-rnsitece.blogspot.com/>

Various activities conducted by COE-VLSI are as follows:

I. The Center initiated the following technical training course with the help of experts from the industries (kanada technologies) for the 5th semester students. The course with hands on experience was conducted. The contents of the various modules are as shown in the following table.

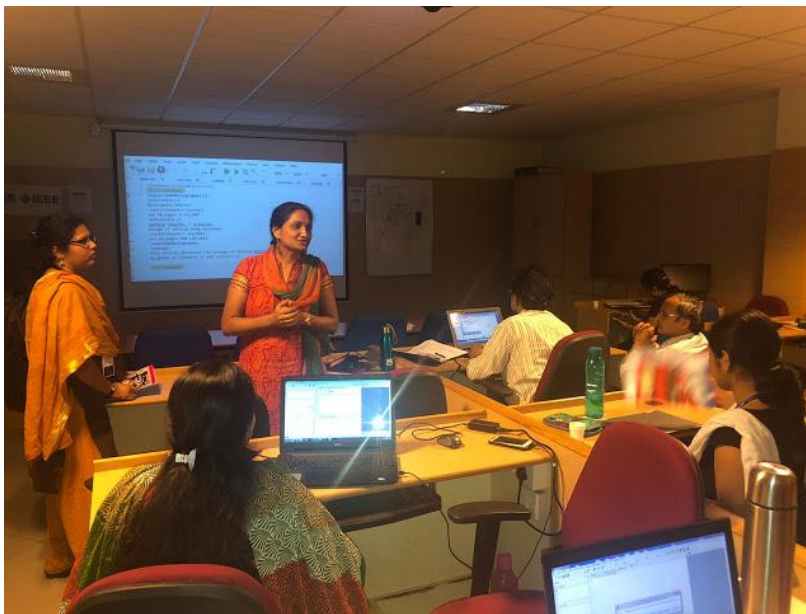
V Semester	V Semester	VI Semester	VII Semester
Module 1	Module 2	Module 3	Module 4
1. Introduction to MOSFET 2. Tool Introduction 3. Custom Layouts 4. Standard Cell Layouts	1. Memory Layouts 2. Analog Layouts 3. Quality Checks 4. Advanced Topics	1.VLSI Certification 2.Mini Projects	1.Internship
70 hours	70 hours	70 hours	4 weeks

- Above program was planned for fifth and sixth semesters.

- Out of above four modules, Modules – 1 and 2 were conducted in 5th semester module 3 during 6th semester and module 4 in 7th semester.

II. Students of 5th semester and two faculty members visited NAL as part of Industry visit.

III. One day workshop on Latex was conducted on 12-04-2019 for the benefit faculty members of RNIT. Dr. Rajini and Mr. Sanjay were the resource persons.





IV. Students of UG/PG and Faculty members have presented papers in the National conference organized by ECE dept, RNSIT, held on 03/05/2019.

V. THREE DAYS WORKSHOP on Analog and Digital VLSI Design for staff.

Organized on 21/06/2018 and 23/06/2018
Venue: Seminar Hall, Dept of ECE, RNSIT.
Coordinators: Dr. Sangeetha B G



V. ONE day workshop on VLSI Design and Simulation for digital Applications for UG students

Organized on 1/05/2019

Venue: VLSI Lab, Dept of ECE, RNSIT

Coordinators: Dr. Sangeetha B G

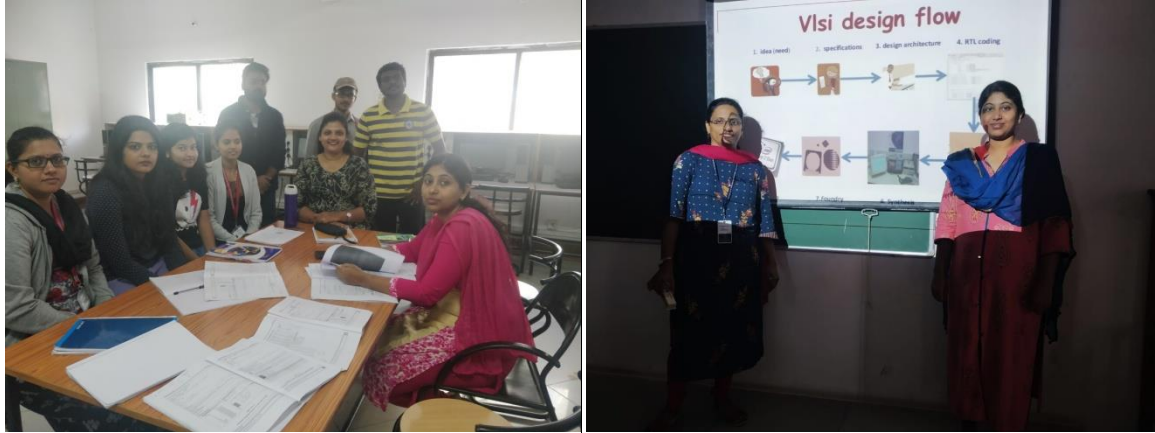
VI. TWO DAYS WORKSHOP on Digital Design and Layouts for students.

Organized on 21/06/2019 and 23/06/2019
Venue: Seminar Hall, Dept of ECE, RNSIT.
Coordinators: Ravi S



VII. TWO DAYS WORKSHOP on Digital VLSI Design for PG students.

Organized on 03/01/2020 and 04/01/2020
Venue: Seminar Hall, Dept of ECE, RNSIT.
Coordinators: Dr. Sangeetha B G and Dr. Smitha



Proposed Activities for the present semester: February to May 2020

4th Semester ECE

1. Two days workshop on Digital Design using Verilog .
2. Two days workshop on VLSI design and simulation using Cadence software.

6th Semester ECE:

1. 200 hours “Advanced VLSI course”, by Industry people.
2. Two days workshop on VLSI Design using Cadence software.